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Kai Hwang is presently a Presidential Chair Professor in Computer Science and Engineering at the Chinese University of Hong Kong (CUHK), Shenzhen, China. He also serves as a Chief Scientist at the Cloud Computing Center, Chinese Academy of Sciences. He has taught at the University of Southern California and at Purdue University for 46 years prior joining CUHK.

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The salient features of the book are as follows:

- Hybrid Elements including topics like Memory organization, Binary representation of data, Computer arithmetic Software for parallel programming, tagged across some chapters through Quick Response (QR) Codes
- Learning objectives tagged across chapters:
- Emphasis on parallelism, scalability and programmability aspects of computer architecture. It presents the analysis of scalability
- Issues related to instruction level parallelism, processor clock speed, and power consumption defined according to the recent developments in processor design
- Inclusion of important topics like processor design, control unit, input and output, parallelis
- erial Bus, Real systems— IBM, Hitachi, Cray, Intel, UltraSparc, Blue Gene (from IBM), Cray



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Exercise Problems: 251

This text presents the latest technologies for parallel processing and high performance computing. It deals with advanced computer architecture and parallel processing systems and techniques, providing an integrated study of computer hardware and software systems, and the material is suitable for use on courses found in computer science, computer engineering, or electrical engineering departments.

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This book covers the syllabus of GGSIPU, DU, UPTU, PTU, MDU, Pune University and many other universities. It is useful for B.Tech(CSE/IT), M.Tech(CSE), MCA(SE) students. Many solved problems have been added to make this book more fresh. It has been divided in three parts :Parallel Algorithms, Parallel Programming and Super Computers.

Computer Architecture/Software Engineering

This book constitutes the refereed proceedings of the 10th Annual Conference on Advanced Computer Architecture,

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ACA 2014, held in Shenyang, China, in August 2014. The 19 revised full papers presented were carefully reviewed and selected from 115 submissions. The papers are organized in topical sections on processors and circuits; high performance computing; GPUs and accelerators; cloud and data centers; energy and reliability; intelligence computing and mobile computing.

This volume is the proceedings of the fifth International Symposium on Algorithms and Computation, ISAAC '94, held in Beijing, China in August 1994. The 79 papers accepted for inclusion in the volume after a careful reviewing process were selected from a total of almost 200 submissions. Besides many internationally renowned

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experts, a number of excellent Chinese researchers present their results to the international scientific community for the first time here. The volume covers all relevant theoretical and many applicational aspects of algorithms and computation.

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