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Researchers from ETH Zurich have integrated photonics and electronics on one chip. "If you convert the electronic signals into light signals using separate chips, you lose a significant amount of signal quality. This also limits the speed of data transmission using light," says ETH researcher, Ueli Koch. The integration was achieved by placing the electronic and photonic components on top of one another, and connecting them through vias.

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This program focuses on integrated electronics, photonic devices and systems and nanoengineering. Our activities span a wide area ranging from the development of materials to the simulation of operation, fabrication and characterization of devices, circuits and systems. We provide advanced micro-and nano-fabrication and characterization services for industrial and academic partners and focus on the development of human potential.

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biology, the demand for portable, lightweight integrated microsystems is relentless. These devices are getting exponentially smaller, increasingly used in everything from video games, hearing aids, and pacemakers to more intricate biomedical engineering and military applications. Edited by Kris Iniewski, a revolutionary in the field of advanced semiconductor materials, *Integrated Microsystems: Electronics, Photonics, and Biotechnology* focuses on techniques for optimized design and fabrication of these intelligent miniaturized devices and systems. Composed of contributions from experts in academia and industry around the world, this reference covers processes compatible with CMOS integrated circuits, which combine computation, communications, sensing, and actuation capabilities. Light on math and physics, with a greater emphasis on microsystem design and configuration and electrical engineering, this book is organized in three sections—Microelectronics and Biosystems, Photonics and Imaging, and Biotechnology and MEMs. It addresses key topics, including physical and chemical sensing, imaging, smart actuation, and data fusion and management. Using tables, figures, and equations to help illustrate concepts, contributors examine and explain the potential of emerging applications for areas including biology, nanotechnology, micro-electromechanical systems (MEMS), microfluidics, and photonics.

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This book describes Microelectromechanical systems (MEMS) technology and demonstrates how MEMS allow miniaturization, parallel fabrication, and efficient packaging of optics, as well as integration of optics and electronics. The book shows how the characteristics of MEMS enable practical implementations of a variety of applications, including projection displays, fiber switches, interferometers,

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and spectrometers. The authors conclude with an up-to-date discussion of the need for the combination of MEMS and Photonic crystals.

From power electronics to power integrated circuits (PICs), smart power technologies, devices, and beyond, *Integrated Power Devices and TCAD Simulation* provides a complete picture of the power management and semiconductor industry. An essential reference for power device engineering students and professionals, the book not only describes the physics inside integrated power semiconductor devices such lateral double-diffused metal oxide semiconductor field-effect transistors (LDMOSFETs), lateral insulated-gate bipolar transistors (LIGBTs), and super junction LDMOSFETs but also delivers a simple introduction to power management systems. Instead of abstract theoretical treatments and daunting equations, the text uses technology computer-aided design (TCAD) simulation examples to explain the design of integrated power semiconductor devices. It also explores next generation power devices such as gallium nitride power high electron mobility transistors (GaN power HEMTs). Including a virtual process flow for smart PIC technology as well as a hard-to-find technology development organization chart, *Integrated Power Devices and TCAD Simulation* gives students and junior engineers a head start in the field of power semiconductor devices while helping to fill the gap between power device engineering and power management systems.

Three-dimensional (3D) integration of microsystems

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and subsystems has become essential to the future of semiconductor technology development. 3D integration requires a greater understanding of several interconnected systems stacked over each other. While this vertical growth profoundly increases the system functionality, it also exponentially increases the design complexity. Design of 3D Integrated Circuits and Systems tackles all aspects of 3D integration, including 3D circuit and system design, new processes and simulation techniques, alternative communication schemes for 3D circuits and systems, application of novel materials for 3D systems, and the thermal challenges to restrict power dissipation and improve performance of 3D systems. Containing contributions from experts in industry as well as academia, this authoritative text: Illustrates different 3D integration approaches, such as die-to-die, die-to-wafer, and wafer-to-wafer Discusses the use of interposer technology and the role of Through-Silicon Vias (TSVs) Presents the latest improvements in three major fields of thermal management for multiprocessor systems-on-chip (MPSoCs) Explores ThruChip Interface (TCI), NAND flash memory stacking, and emerging applications Describes large-scale integration testing and state-of-the-art low-power testing solutions Complete with experimental results of chip-level 3D integration schemes tested at IBM and case studies on advanced complementary metal-oxide-semiconductor (CMOS) integration for 3D integrated circuits (ICs), Design of 3D Integrated Circuits and Systems is a practical reference that not only covers a wealth of design issues encountered in 3D integration but also demonstrates their impact on the efficiency of 3D systems.

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In our abundant computing infrastructure, performance improvements across most all application spaces are now severely limited by the energy dissipation involved in processing, storing, and moving data. The exponential increase in the volume of data to be handled by our computational infrastructure is driven in large part by unstructured data from countless sources. This book explores revolutionary device concepts, associated circuits, and architectures that will greatly extend the practical engineering limits of energy-efficient computation from device to circuit to system level. With chapters written by international experts in their corresponding field, the text investigates new approaches to lower energy requirements in computing. Features

- Has a comprehensive coverage of various technologies
- Written by international experts in their corresponding field
- Covers revolutionary concepts at the device, circuit, and system levels

Power Management Integrated Circuits and Technologies delivers a modern treatise on mixed-signal integrated circuit design for power management. Comprised of chapters authored by leading researchers from industry and academia, this definitive text: Describes circuit- and architectural-level innovations that meet advanced power and speed capabilities Explores hybrid inductive-capacitive converters for wide-range dynamic voltage scaling Presents innovative control techniques for single inductor dual output (SIDO) and single inductor multiple output (SIMO) converters Discusses cutting-edge design techniques including switching

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converters for analog/RF loads. Compares the use of GaAs pHEMTs to CMOS devices for efficient high-frequency switching converters. Thus, Power Management Integrated Circuits and Technologies provides comprehensive, state-of-the-art coverage of this exciting and emerging field of engineering.

Advances in design methods and process technologies have resulted in a continuous increase in the complexity of integrated circuits (ICs). However, the increased complexity and nanometer-size features of modern ICs make them susceptible to manufacturing defects, as well as performance and quality issues. Testing for Small-Delay Defects in Nanoscale CMOS Integrated Circuits covers common problems in areas such as process variations, power supply noise, crosstalk, resistive opens/bridges, and design-for-manufacturing (DfM)-related rule violations. The book also addresses testing for small-delay defects (SDDs), which can cause immediate timing failures on both critical and non-critical paths in the circuit. Overviews semiconductor industry test challenges and the need for SDD testing, including basic concepts and introductory material. Describes algorithmic solutions incorporated in commercial tools from Mentor Graphics. Reviews SDD testing based on "alternative methods" that explores new metrics, top-off ATPG, and circuit topology-based solutions. Highlights the advantages and disadvantages of a diverse set of metrics, and identifies scope for improvement. Written from the triple viewpoint of university researchers, EDA tool developers, and chip designers and tool users, this book is the first of its kind to address all aspects of SDD testing from such a

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diverse perspective. The book is designed as a one-stop reference for current industrial practices, research challenges in the domain of SDD testing, and recent developments in SDD solutions.

For decades, people have searched for ways to harvest energy from natural sources. Lately, a desire to address the issue of global warming and climate change has popularized solar or photovoltaic technology, while piezoelectric technology is being developed to power handheld devices without batteries, and thermoelectric technology is being explored to convert wasted heat, such as in automobile engine combustion, into electricity. Featuring contributions from international researchers in both academics and industry, *Energy Harvesting with Functional Materials and Microsystems* explains the growing field of energy harvesting from a materials and device perspective, with resulting technologies capable of enabling low-power implantable sensors or a large-scale electrical grid. In addition to the design, implementation, and components of energy-efficient electronics, the book covers current advances in energy-harvesting materials and technology, including: High-efficiency solar technologies with lower cost than existing silicon-based photovoltaics Novel piezoelectric technologies utilizing mechanical energy from vibrations and pressure The ability to harness thermal energy and temperature profiles with thermoelectric materials Whether you're a practicing engineer, academician, graduate student, or entrepreneur looking to invest in energy-harvesting devices, this book is your complete guide to fundamental materials and applied

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